

FIG. 1



FIG. 2

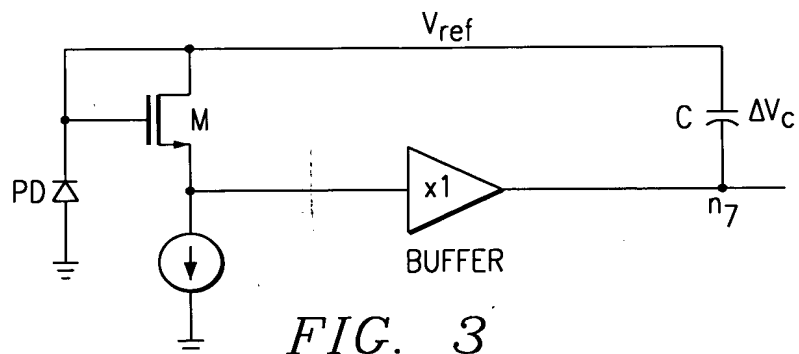
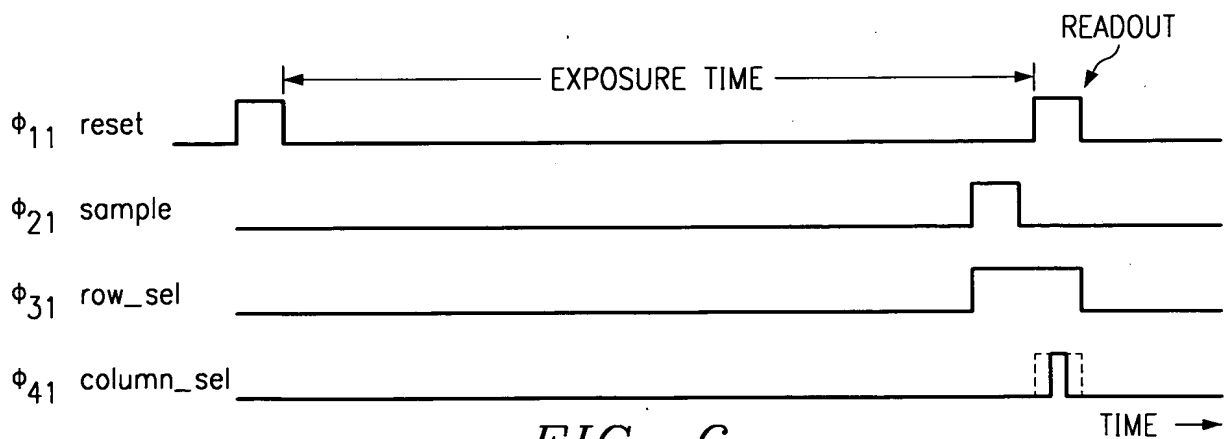
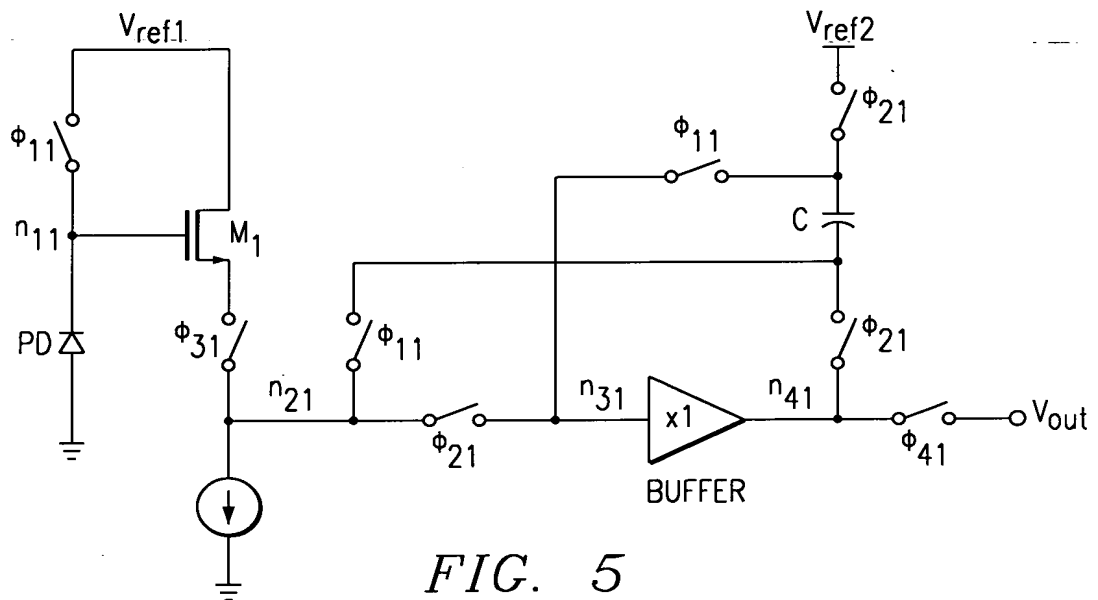
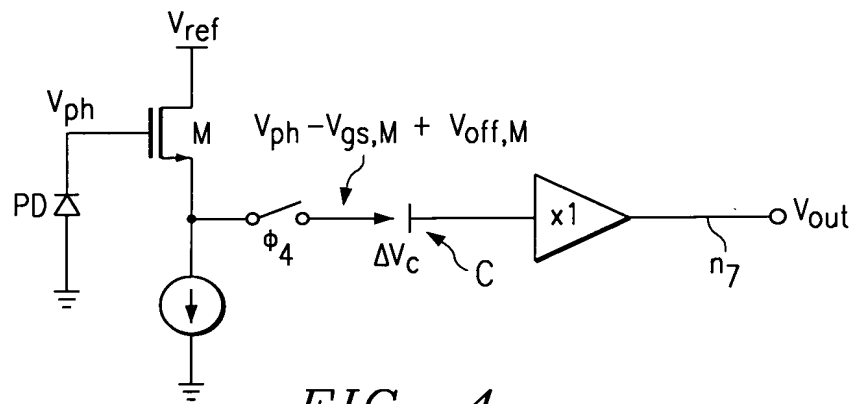


FIG. 3



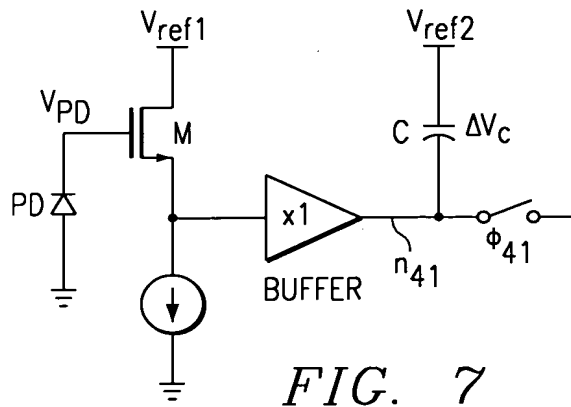


FIG. 7

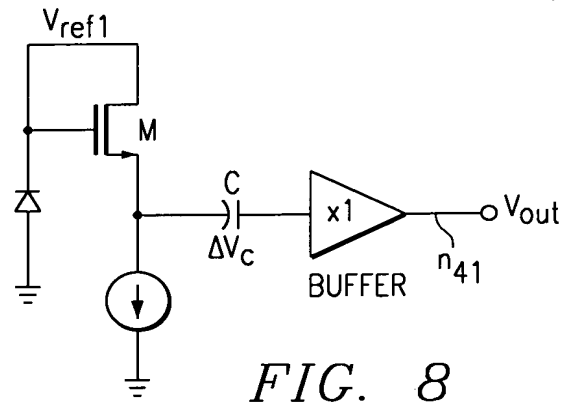


FIG. 8

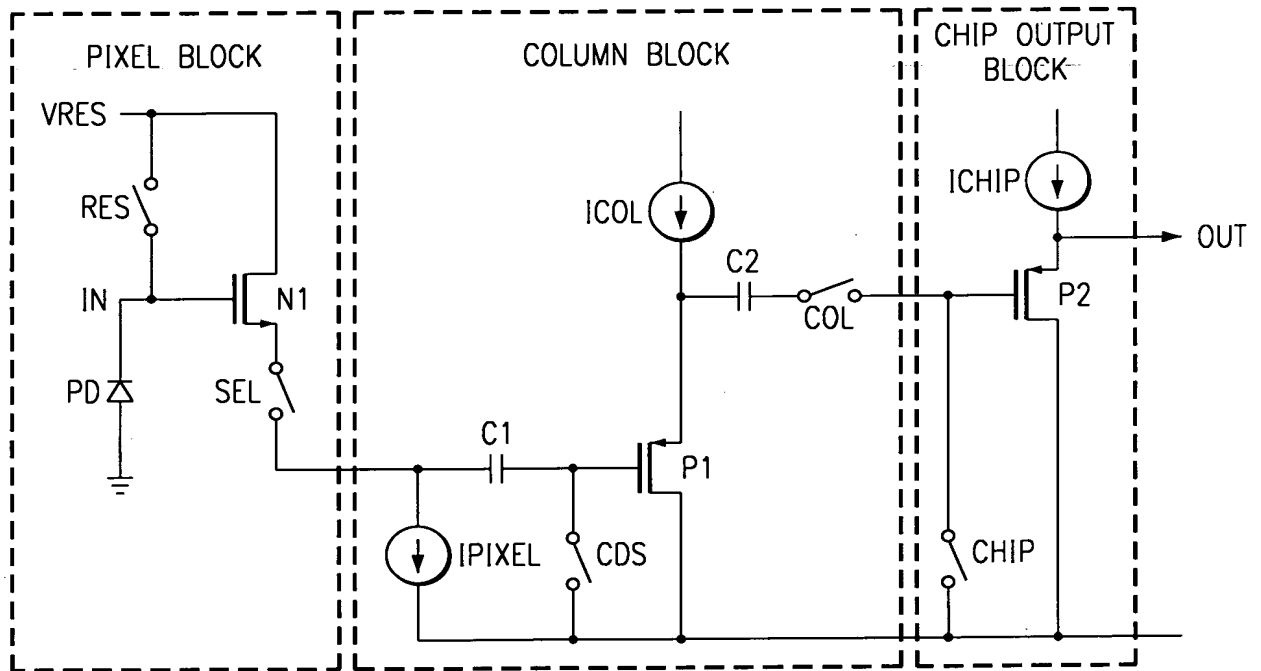


FIG. 9 (PRIOR ART)

	RES	SEL	CDS	COL	CHIP
INTEGRATION PERIOD	OPEN	OPEN	X	X	X
READOUT STEP 1	OPEN	OPEN	CLOSED	CLOSED	CLOSED
READOUT STEP 2	OPEN	CLOSED	CLOSED	OPEN	CLOSED
READOUT STEP 3	OPEN	CLOSED	OPEN	OPEN	OPEN
READOUT STEP 4	CLOSED	CLOSED	OPEN	CLOSED	OPEN
READOUT STEP 5	OPEN	OPEN	OPEN	OPEN	CLOSED
READY FOR NEXT PIXEL READOUT					

FIG. 10 (PRIOR ART)